TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (π-MOSV)

## **2SJ567**

#### **Switching Applications**

# Chopper Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: RDS (ON) = 1.6  $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 2.0 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = -100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = -200 \,\text{V})$
- Enhancement model:  $V_{th} = -1.5 \sim -3.5 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA)}$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
Drain-source voltage			$V_{DSS}$	-200	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )			$V_{DGR}$	-200	٧	
Gate-source voltage			$V_{GSS}$	±20	V	
Drain current	DC (	Note 1)	I <sub>D</sub>	-2.5	Α	
Diam curient	Pulse (	Note 1)	I <sub>DP</sub>	-10	A	
Drain power dissipation (Tc = 25°C)			P <sub>D</sub>	20	W	
Single-pulse avalanche energy (Note 2)			E <sub>AS</sub>	97.5	mJ	
Avalanche current			I <sub>AR</sub>	-2.5	Α	
Repetitive avalanche energy (Note 3)			E <sub>AR</sub>	2.0	mJ	
Channel temperature			T <sub>ch</sub>	150	°C	
Storage temperature range			T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

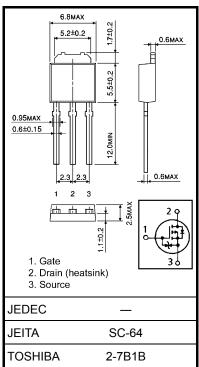
#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	6.25	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W

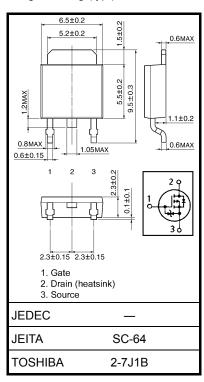
- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2:  $V_{DD} = -50$  V, Tch = 25°C (initial), L = -25.2 mH,  $I_{AR} = -2.5$  A  $R_G = 25~\Omega$
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.36 g (typ.)



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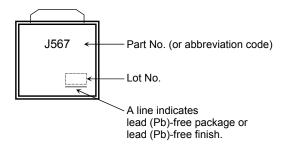
## Electrical Characteristics (Ta = 25°C)

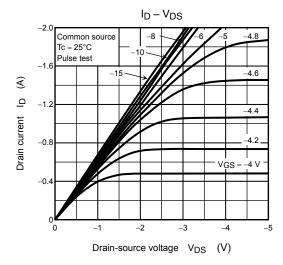
Char	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cutoff curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = -200 V, V <sub>GS</sub> = 0 V	_	_	-100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-200	_	_	V
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.5	_	-3.5	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.5 \text{ A}$	_	1.6	2.0	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -1.5 \text{ A}$	1.0	2.0	_	S
Input capacitance	9	C <sub>iss</sub>		_	410	_	
Reverse transfer	capacitance	C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	40	_	pF
Output capacitan	Output capacitance			_	145	_	
Rise time	Rise time	t <sub>r</sub>	0 V ¬	_	20	_	
Cuitabina tima	Turn-on time	t <sub>on</sub>	$V_{GS}$ $V$	_	45	_	20
Switching time	Fall time	t <sub>f</sub>		_	15	_	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_W = 10 \mu s$ $V_{DD}^{\sim} \simeq -100 \text{ V}$	_	85	_	
Total gate charge (Gate source plus		Qg	$V_{DD} \simeq -160 \text{ V}, V_{GS} = -10 \text{ V},$	_ 10 _		_	nC
Gate-source charge Gate-drain ("Miller") charge		Q <sub>gs</sub>	$I_D = -2.5 \text{ A}$	_	6	_	
		Q <sub>gd</sub>		_	4	_	

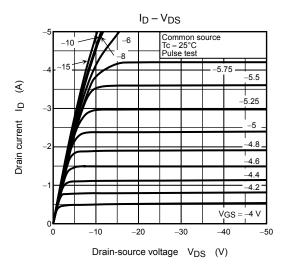
## Source-Drain Ratings and Characteristics (Ta = 25°C)

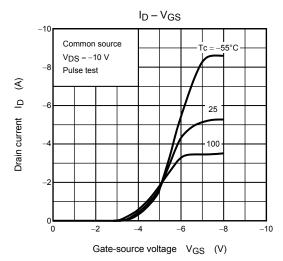
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)	I <sub>DR</sub>	_	_	_	-2.5	Α
Pulse drain reverse current	(Note 1)	I <sub>DRP</sub>	_	_	_	-10	Α
Forward voltage (diode)		V <sub>DSF</sub>	I <sub>DR</sub> = -2.5 A, V <sub>GS</sub> = 0 V	_	_	2.0	V
Reverse recovery time		t <sub>rr</sub>	$I_{DR} = -2.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	135	_	ns
Reverse recovery charge		Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	0.81	_	μС

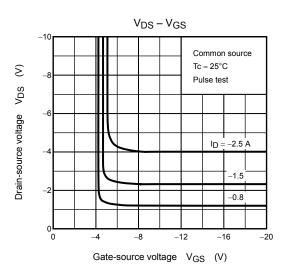
### Marking

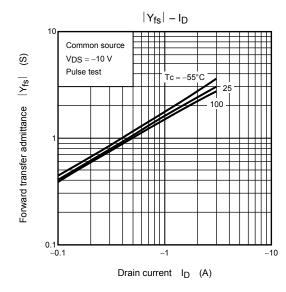


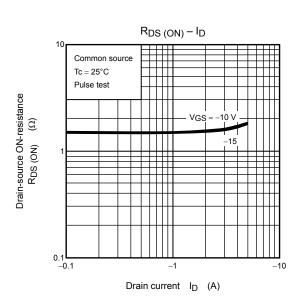


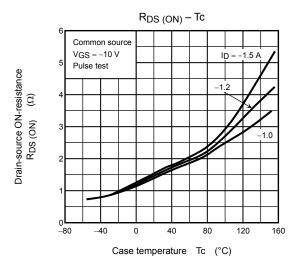


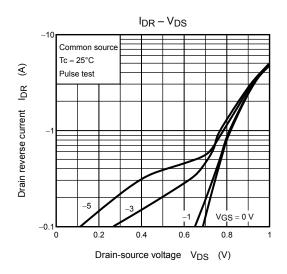


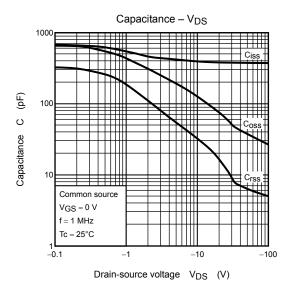


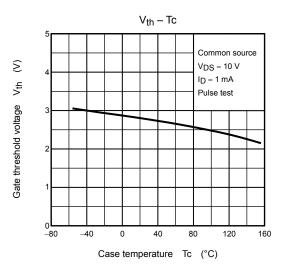


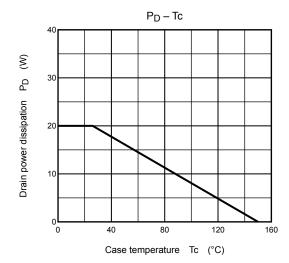


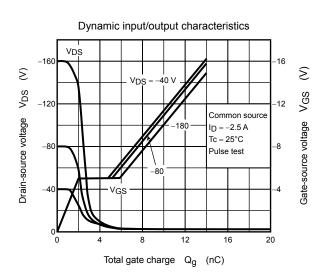


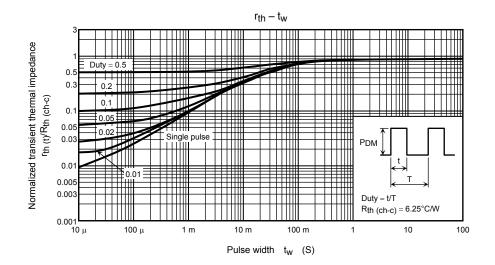


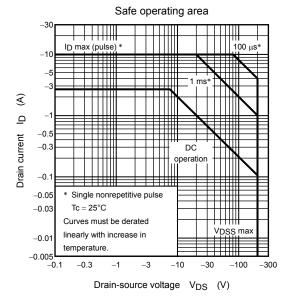


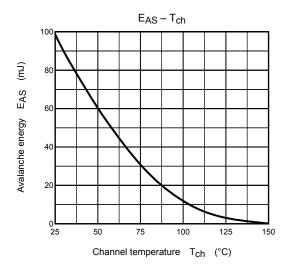


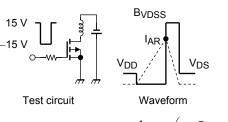












$$R_G = 25 \Omega$$
  
 $V_{DD} = -50 \text{ V, L} = 25.2 \text{ mH}$   $E_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{l}^2 \cdot \left(\frac{\text{BVDSS}}{\text{BVDSS} - \text{VDD}}\right)$ 

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