

BCR1AM-12A

Triac

Low Power Use

REJ03G1248-0200

Rev.2.00

Nov 30, 2007

Features

- $I_{T(RMS)}$: 1 A
- V_{DRM} : 600 V
- I_{FGTI} , I_{RGTI} , $I_{RGT III}$: 7 mA
- Non-Insulated Type
- Planar Passivation Type

Outline

RENESAS Package code: PRSS0003EA-A
(Package name: TO-92)



1. T₁ Terminal
2. T₂ Terminal
3. Gate Terminal

Applications

Contactless AC switch, fan motor, rice-cooker, electric pot, air cleaner, heater, refrigerator, washing machine, electric fan, vending machine, trigger circuit for low and medium triac, and other general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
		12	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600	V
Non-repetitive peak off-state voltage ^{Note1}	V_{DSM}	720	V

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	$I_T (RMS)$	1.0	A	Commercial frequency, sine full wave 360° conduction, $T_c = 56^\circ C$ ^{Note3}
Surge on-state current	I_{TSM}	10	A	60Hz sinewave 1 full cycle, peak value, non-repetitive
I^2t for fusing	I^2t	0.41	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	1	W	
Average gate power dissipation	$P_{G(AV)}$	0.1	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I_{GM}	0.5	A	
Junction temperature	T_j	- 40 to +125	°C	
Storage temperature	T_{stg}	- 40 to +125	°C	
Mass	—	0.23	g	Typical value

Notes: 1. Gate open.

Electrical Characteristics

Parameter	Symbol	Rated value			Unit	Test conditions	
		Min.	Typ.	Max.			
Repetitive peak off-state current	I_{DRM}	—	—	0.5	mA	$T_j = 125^\circ C$, V_{DRM} applied	
On-state voltage	V_{TM}	—	—	1.6	V	$T_c = 25^\circ C$, $I_{TM} = 1.5 A$, Instantaneous measurement	
Gate trigger voltage ^{Note2}	I	V_{FGTI}	—	—	2.0	V	$T_j = 25^\circ C$, $V_D = 6 V$, $R_L = 6 \Omega$, $R_G = 330 \Omega$
	II	V_{RGTI}	—	—	2.0	V	
	III	V_{RGTIII}	—	—	2.0	V	
Gate trigger current ^{Note2}	I	I_{FGTI}	—	—	7	mA	$T_j = 25^\circ C$, $V_D = 6 V$, $R_L = 6 \Omega$, $R_G = 330 \Omega$
	II	I_{RGTI}	—	—	7	mA	
	III	I_{RGTIII}	—	—	7	mA	
Gate non-trigger voltage	V_{GD}	0.1	—	—	V	$T_j = 125^\circ C$, $V_D = 1/2 V_{DRM}$	
Thermal resistance	$R_{th(j-c)}$	—	—	50	°C/W	Junction to case ^{Note3}	
Critical-rate of rise of off-state commutating voltage ^{Note4}	$(dv/dt)_c$	2	—	—	V/ μs	$T_j = 125^\circ C$	

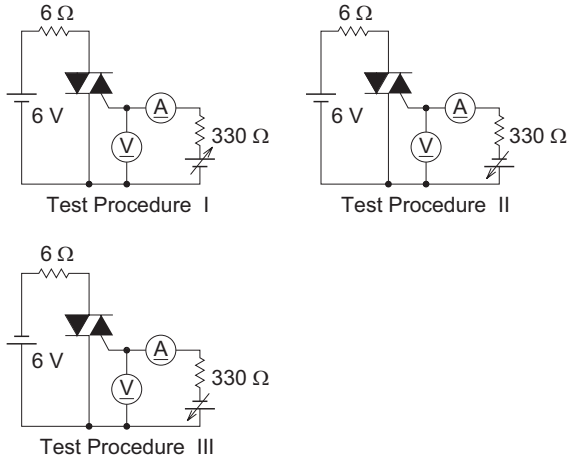
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

3. Case temperature is measured at the T_2 terminal 1.5 mm away from the molded case.

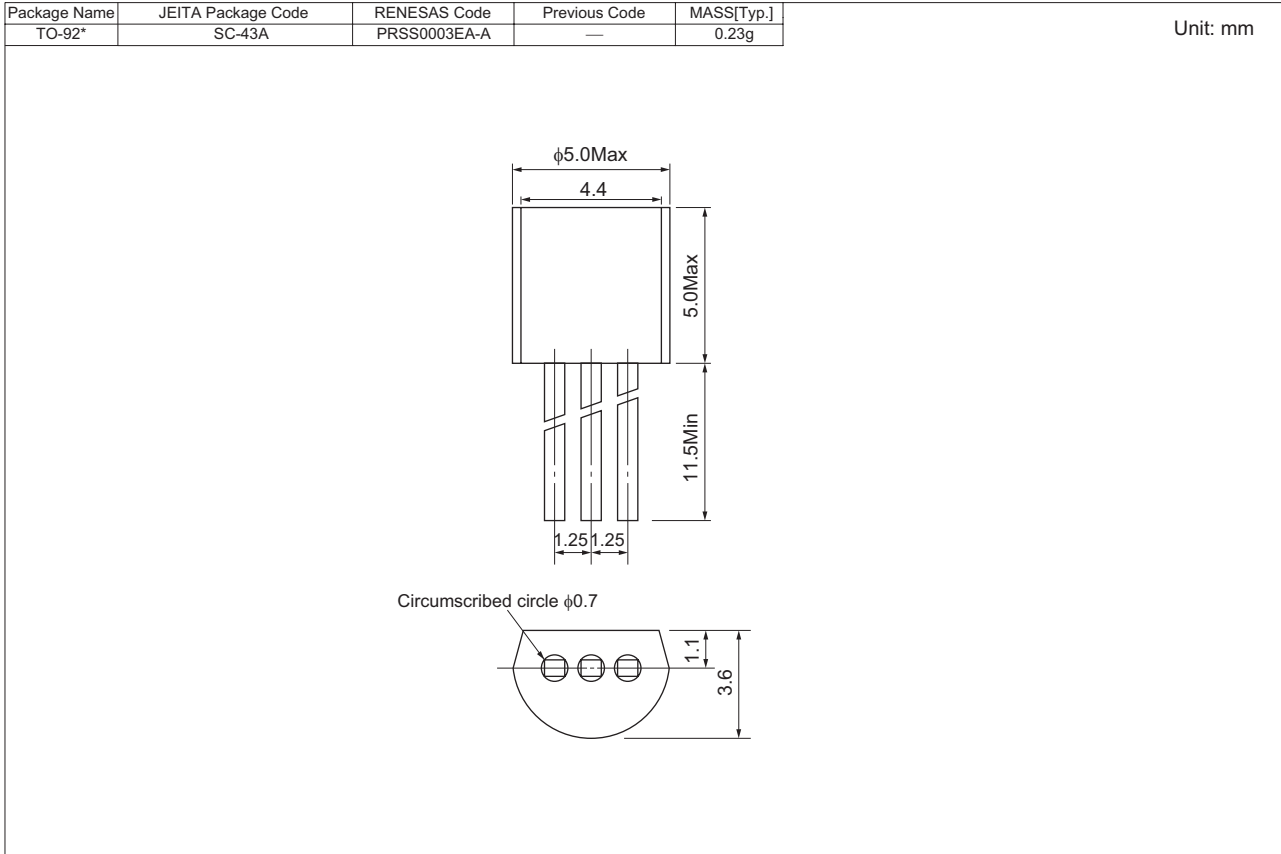
4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature $T_j = 125^\circ C$ 2. Rate of decay of on-state commutating current $(di/dt)_c = - 0.5 A/ms$ 3. Peak off-state voltage $V_D = 400 V$	

Gate Trigger Characteristics Test Circuits



Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	BCR1AM-12A
Lead form	Vinyl sack	500	Type name – Lead forming code	BCR1AM-12A-A6
Form A8	Taping	2000	Type name – TB	BCR1AM-12A-TB

Note : Please confirm the specification about the shipping in detail.

Notes:

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510