

General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® E series is optimized for its switching characteristics to achieve balance between EMI and efficiency. It is designed to enable power supply systems to reach the highest efficiency while still meeting EMI standards.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- EMI and performance balanced



Applications

- LED lighting
- Charger
- Adapter
- TV power
- Telecom power
- Server power
- Solar/UPS

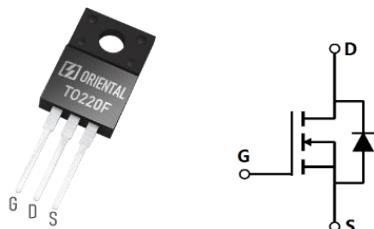
Key Performance Parameters

Parameter	Value	Unit
V_{DS} , min @ $T_{j(max)}$	700	V
I_D , pulse	45	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	290	mΩ
Q_g	21	nC

Marking Information

Product Name	Package	Marking
OSG65R290FEF	TO220F	OSG65R290FE

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25\text{ }^\circ\text{C}$	I_D	15	A
Continuous drain current ¹⁾ , $T_C=100\text{ }^\circ\text{C}$		9.5	
Pulsed drain current ²⁾ , $T_C=25\text{ }^\circ\text{C}$	$I_{D,\text{pulse}}$	45	A
Continuous diode forward current ¹⁾ , $T_C=25\text{ }^\circ\text{C}$	I_S	15	A
Diode pulsed current ²⁾ , $T_C=25\text{ }^\circ\text{C}$	$I_{S,\text{pulse}}$	45	A
Power dissipation ³⁾ , $T_C=25\text{ }^\circ\text{C}$	P_D	32	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	250	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 480\text{ V}$, $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	3.9	$^\circ\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	$V_{GS}=0\text{ V}$, $I_D=250\text{ }\mu\text{A}$
		700				$V_{GS}=0\text{ V}$, $I_D=250\text{ }\mu\text{A}$, $T_j=150\text{ }^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.9		3.9	V	$V_{DS}=V_{GS}$, $I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		0.25	0.29	Ω	$V_{GS}=10\text{ V}$, $I_D=7.5\text{ A}$
			0.68			$V_{GS}=10\text{ V}$, $I_D=7.5\text{ A}$, $T_j=150\text{ }^\circ\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=650\text{ V}$, $V_{GS}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		1079		pF	V _{GS} =0 V, V _{DS} =50 V, f=100 kHz
Output capacitance	C _{oss}		74.1		pF	
Reverse transfer capacitance	C _{rss}		2.1		pF	
Turn-on delay time	t _{d(on)}		30.1		ns	V _{GS} =10 V, V _{DS} =400 V, R _G =2 Ω, I _D =8 A
Rise time	t _r		19.5		ns	
Turn-off delay time	t _{d(off)}		61.5		ns	
Fall time	t _f		15.5		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		21.0		nC	V _{GS} =10 V, V _{DS} =400 V, I _D =8 A
Gate-source charge	Q _{gs}		5.0		nC	
Gate-drain charge	Q _{gd}		7.4		nC	
Gate plateau voltage	V _{plateau}		5.5		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.3	V	I _S =15 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		262.4		ns	
Reverse recovery charge	Q _{rr}		2.9		μC	
Peak reverse recovery current	I _{rrm}		23.4		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) V_{DD}=100 V, V_{GS}=10 V, L=10 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams

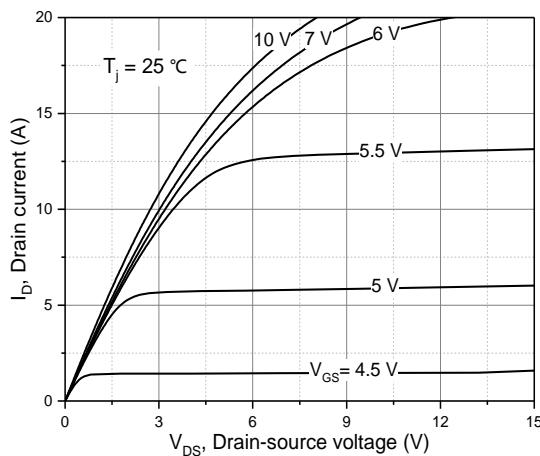


Figure 1. Typ. output characteristics

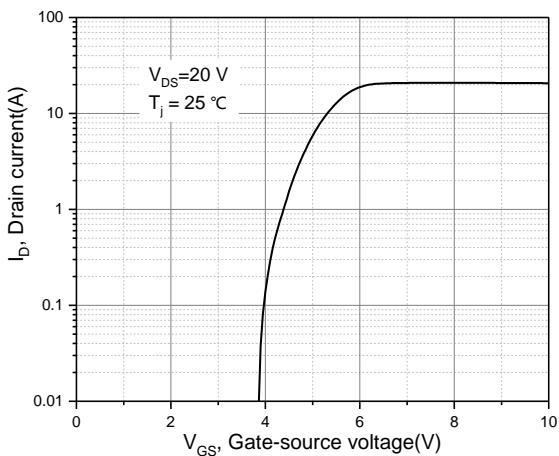


Figure 2. Typ. transfer characteristics

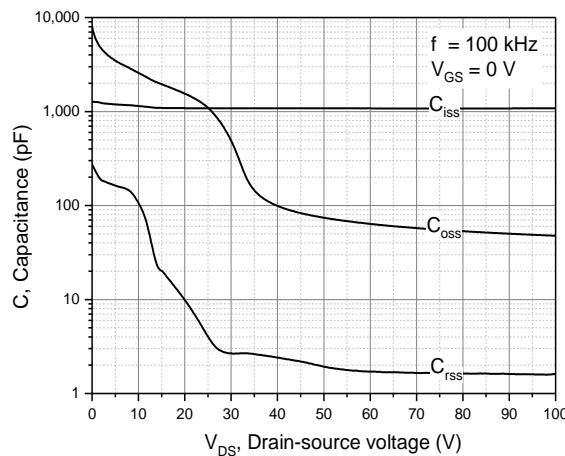


Figure 3. Typ. capacitances

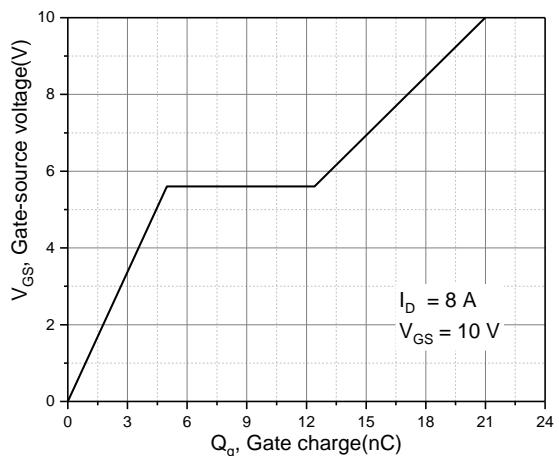


Figure 4. Typ. gate charge

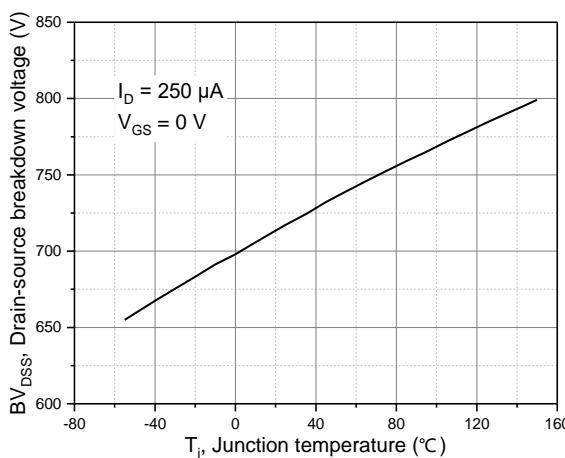


Figure 5. Drain-source breakdown voltage

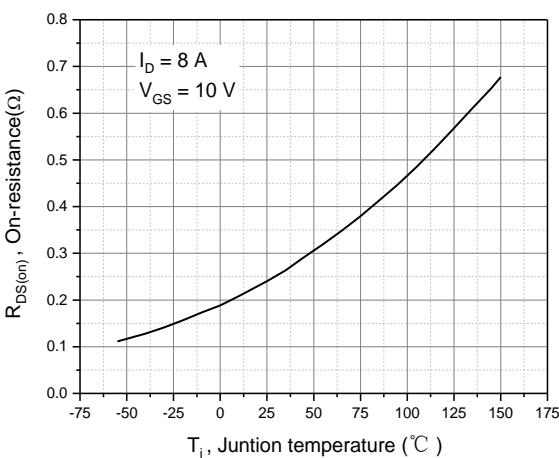
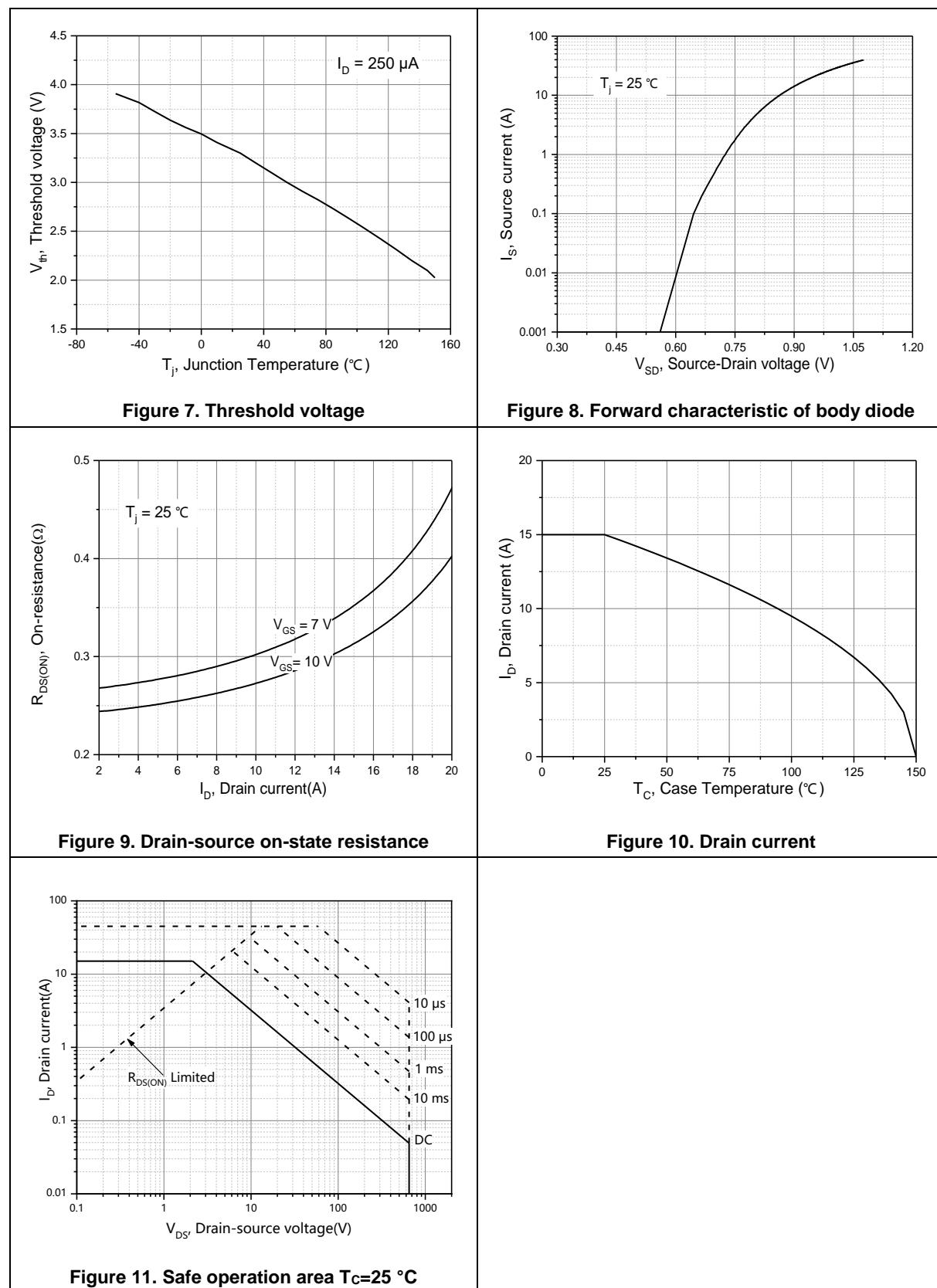


Figure 6. Drain-source on-state resistance



Test circuits and waveforms



Figure 1. Gate charge test circuit & waveform

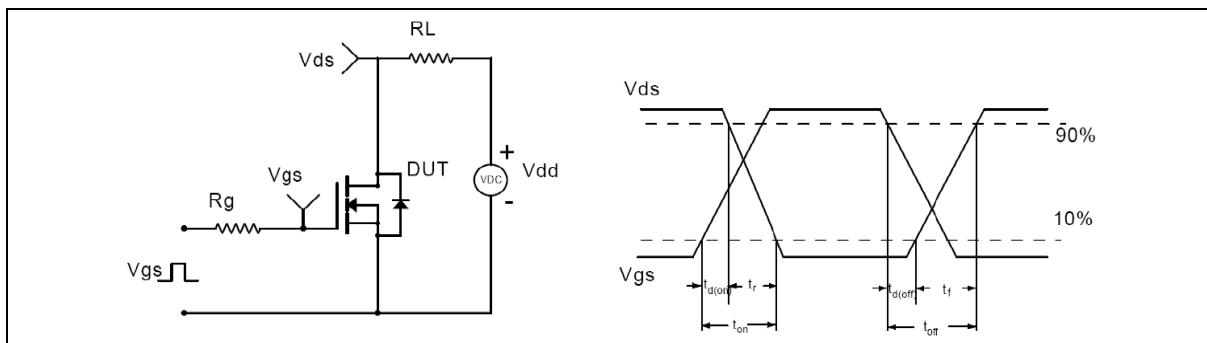


Figure 2. Switching time test circuit & waveforms

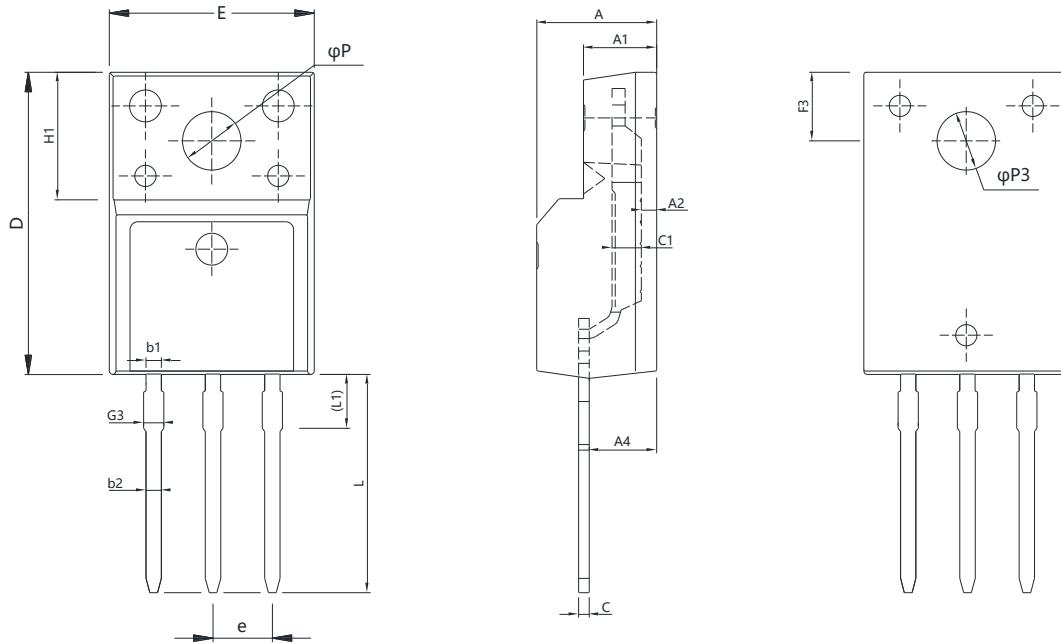


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

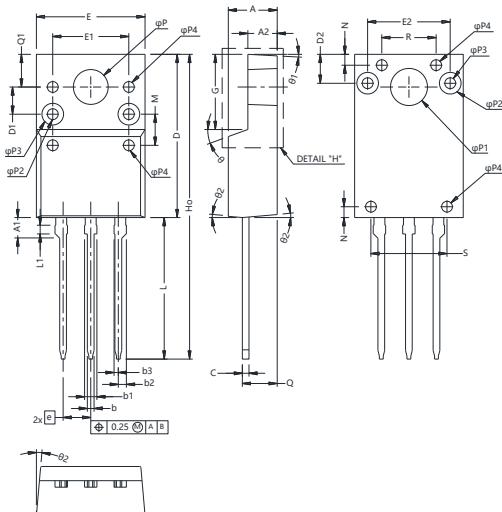
Package Information



Symbol	mm		
	Min	Nom	Max
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70REF		
e	2.54BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95

Version 1: TO220F-P package outline dimension

Package Information



Symbol	mm	
	Min	Max
A	4.30	4.70
A1	1.80	2.10
A2	2.50	2.90
b	0.54	0.84
b1	0.99	1.29
b2	0.56	0.93
b3	0.24	0.55
c	0.49	0.79
D	14.70	15.30
D1	2.50	
D2	2.60	
e	2.29	2.79
E	9.70	10.30
E1	7.00	
E2	7.60	
G	6.70	7.10
H0	28.00	
L	12.50	13.50
L1	0.70	0.90
M	2.86	
N	1.00	
ΦP	3.05	3.40
ΦP1	3.40	
ΦP2	1.00	
ΦP3	2.00	
ΦP4	1.00	
Q	3.10	3.30
Q1	2.70	3.30
R	5.00	
S	7.00	
θ	20°	
θ1	3°	
θ2	5°	

Version 2: TO220F-S package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO220F-P	50	20	1000	6	6000
TO220F-S	50	20	1000	6	6000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R290FEF	TO220F	yes	yes	yes

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