

Data Sheet January 2000 File Number 3750.2

## 6A, 600V Ultrafast Diodes

The RURD660 and RURD660S are ultrafast diodes with soft recovery characteristics ( $t_{rr} < 55$ ns). They have low forward voltage drop and are silicon nitride passivated ion-implanted epitaxial planar construction.

These devices are intended for use as freewheeling/ clamping diodes and rectifiers in a variety of switching power supplies and other power switching applications. Their low stored charge and ultrafast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Formerly developmental type TA49038.

## **Ordering Information**

PART NUMBER	PACKAGE	BRAND
RURD660	TO-251	RUR660
RURD660S	TO-252	RUR660

NOTE: When ordering, use the entire part number. Add the suffix 9A to obtain the TO-252 variant in the tape and reel, i.e., RURD660S9A.

## Symbol



#### **Features**

•	Ultrafast with Soft Recovery
•	Operating Temperature
•	Reverse Voltage

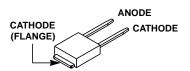
- · Avalanche Energy Rated
- Planar Construction

## **Applications**

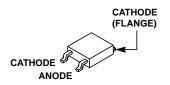
- · Switching Power Supplies
- Power Switching Circuits
- · General Purpose

## **Packaging**

**JEDEC STYLE TO-251** 



JEDEC STYLE TO-252



RURD660

#### **Absolute Maximum Ratings** T<sub>C</sub> = 25°C, Unless Otherwise Specified

		RURD660S	LIMITO
		KUKU0003	UNITS
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	600	V
Working Peak Reverse Voltage	V <sub>RWM</sub>	600	V
DC Blocking Voltage	V <sub>R</sub>	600	V
Average Rectified Forward Current	I <sub>F(AV)</sub>	6	Α
Repetitive Peak Surge Current	····· I <sub>FRM</sub>	12	Α
Nonrepetitive Peak Surge Current	I <sub>FSM</sub>	60	Α
Maximum Power Dissipation	P <sub>D</sub>	50	W
Avalanche Energy (See Figures 10 and 11)	E <sub>AVL</sub>	10	mJ
Operating and Storage Temperature	T <sub>STG</sub> , T <sub>J</sub>	-65 to 175	°С
Maximum Lead Temperature for Soldering			
Leads at 0.063 in. (1.6mm) from case for 10s	T <sub>L</sub>	300	°C
Package Body for 10s, see Tech Brief 334.	T <sub>PKG</sub>	260	oC

### RURD660, RURD660S

**Electrical Specifications**  $T_C = 25^{\circ}C$ , Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V <sub>F</sub>	I <sub>F</sub> = 6A	-	-	1.5	V
	$I_F = 6A, T_C = 150^{\circ}C$	-	-	1.2	V
I <sub>R</sub>	V <sub>R</sub> = 600V	-	-	100	μΑ
	$V_R = 600V, T_C = 150^{\circ}C$	-	-	500	μΑ
t <sub>rr</sub>	$I_F = 1A$ , $dI_F/dt = 200A/\mu s$	-	-	55	ns
	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	-	60	ns
t <sub>a</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	28	-	ns
t <sub>b</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	16	-	ns
Q <sub>RR</sub>	$I_F = 6A$ , $dI_F/dt = 200A/\mu s$	-	150	-	nC
СЈ	V <sub>R</sub> = 10V, I <sub>F</sub> = 0A	-	25	-	pF
$R_{ heta JC}$		-	-	3	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300 $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a$  +  $t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

 $Q_{RR}$  = Reverse recovery charge.

 $C_J$  = Junction capacitance.

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = Pulse width.

D = Duty cycle.

# **Typical Performance Curves**

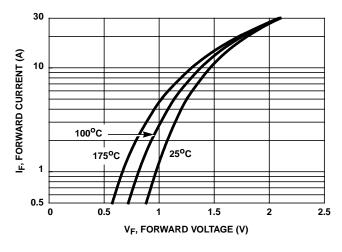


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

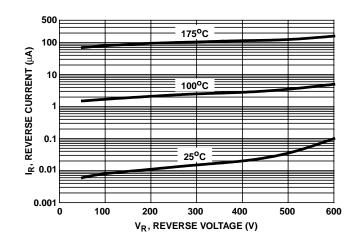


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

## Typical Performance Curves (Continued)

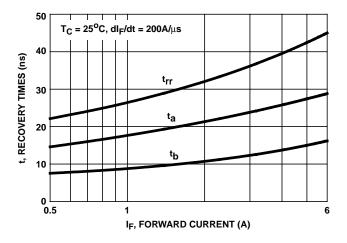


FIGURE 3.  $t_{rr}$ ,  $t_a$  and  $t_b$  curves vs forward current

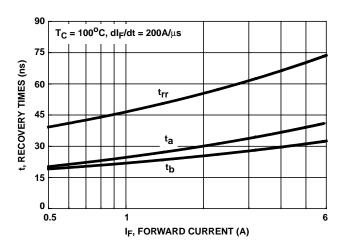


FIGURE 4. t<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

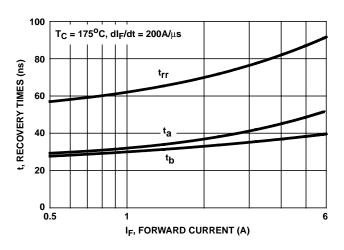


FIGURE 5.  $t_{rr}$ ,  $t_a$  and  $t_b$  curves vs forward current

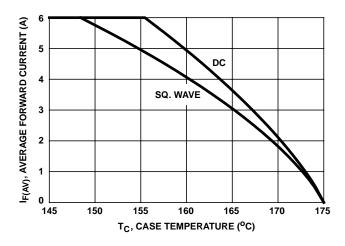


FIGURE 6. CURRENT DERATING CURVE

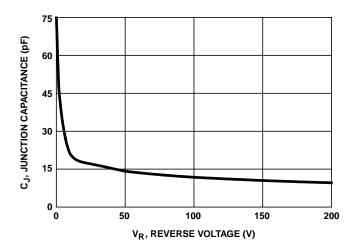


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

3

#### Test Circuits and Waveforms

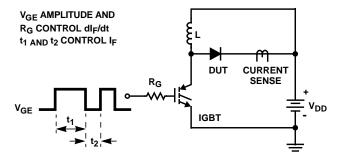


FIGURE 8. t<sub>rr</sub> TEST CIRCUIT

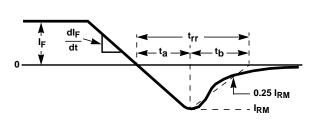


FIGURE 9. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

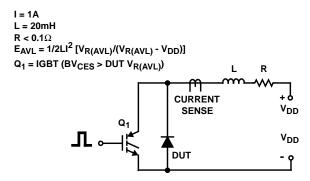


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

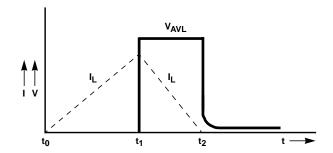


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

All Intersil semiconductor products are manufactured, assembled and tested under ISO9000 quality systems certification.

Intersil semiconductor products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.