

4V Drive Nch+Nch MOSFET

SP8K32

●Structure

Silicon N-channel
MOSFET

●Features

- 1) Built-in G-S Protection Diode.
- 2) Small surface Mount Package (SOP8).

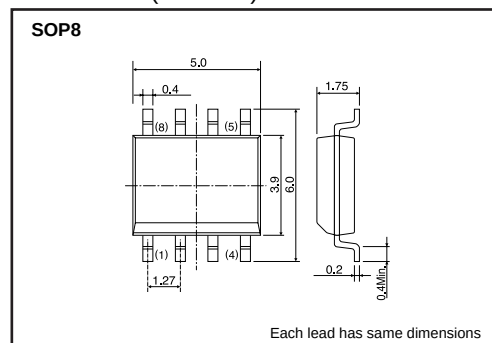
●Applications

Switching

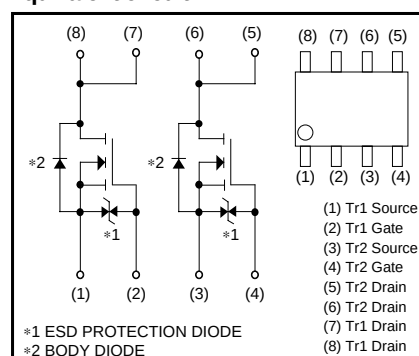
●Packaging specifications

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
SP8K32		○

●Dimensions (Unit : mm)



●Equivalent circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

●Absolute maximum ratings (Ta=25°C)

<It is the same ratings for the Tr1 and Tr2.>

Parameter	Symbol	Limits	Unit
Drain-source voltage	V_{DS}	60	V
Gate-source voltage	V_{GS}	± 20	V
Drain current	Continuous	I_D	A
	Pulsed	I_{DP} *1	A
Source current (Body diode)	Continuous	I_S	A
	Pulsed	I_{SP} *1	A
Total power dissipation	P_D *2	2.0	W/TOTAL
Channel temperature	T_{ch}	150	°C
Range of storage temperature	T_{stg}	-55 to +150	°C

*1 $P_w \leq 10 \mu s$, Duty cycle $\leq 1\%$

*2 Mounted on a ceramic board.

Transistor

●Electrical characteristics (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	—	—	±10	μA	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	60	—	—	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	—	2.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state resistance	R _{DS (on)} *	—	46	65	mΩ	I _D = 4.5A, V _{GS} = 10V
		—	52	73	mΩ	I _D = 4.5A, V _{GS} = 4.5V
		—	55	77	mΩ	I _D = 4.5A, V _{GS} = 4.0V
Forward transfer admittance	Y _{fs} *	4.0	—	—	S	V _{DS} = 10V, I _D = 4.5A
Input capacitance	C _{iss}	—	500	—	pF	V _{DS} = 10V
Output capacitance	C _{oss}	—	120	—	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	—	55	—	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	—	12	—	ns	V _{DD} ≒ 30V
Rise time	t _r *	—	18	—	ns	I _D = 2.3A
Turn-off delay time	t _{d (off)} *	—	40	—	ns	V _{GS} = 10V
Fall time	t _f *	—	13	—	ns	R _L = 13Ω
Total gate charge	Q _g *	—	7.0	10	nC	V _{DD} ≒ 30V, V _{GS} = 5V
Gate-source charge	Q _{gs} *	—	1.6	—	nC	I _D = 4.5A
Gate-drain charge	Q _{gd} *	—	2.5	—	nC	R _L = 6.7Ω, R _G = 10Ω

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

<It is the same characteristics for the Tr1 and Tr2.>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	—	—	1.2	V	I _S =4.5A, V _{GS} =0V

*Pulsed

Transistor

●Electrical characteristic curves

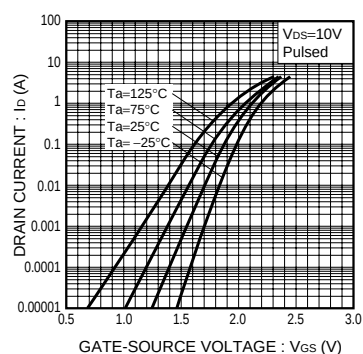


Fig.1 Typical Transfer Characteristics

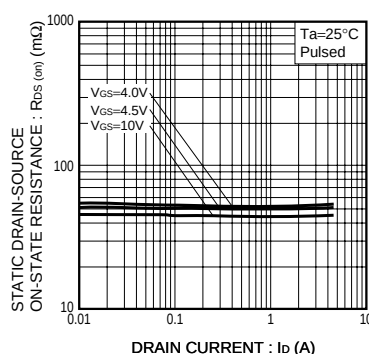


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current(I)

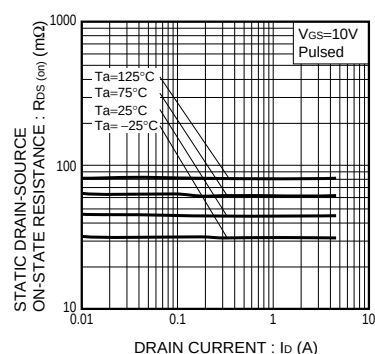


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current(II)

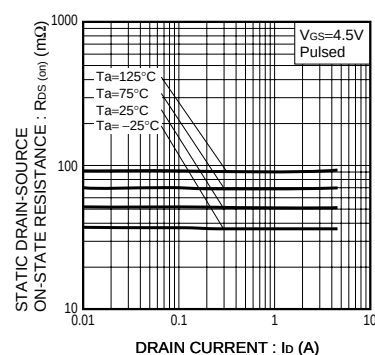


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(III)

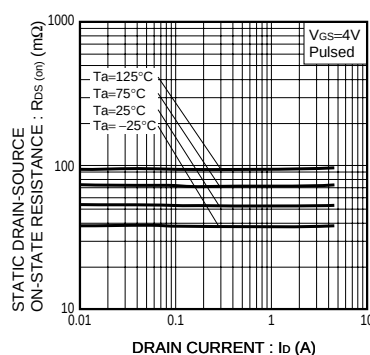


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(IV)

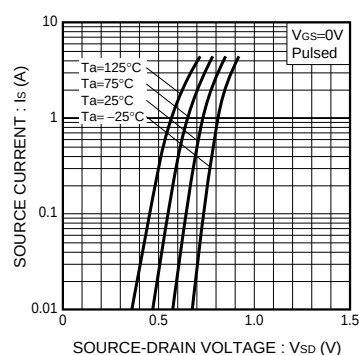


Fig.6 Source Current vs. Source-Drain Voltage

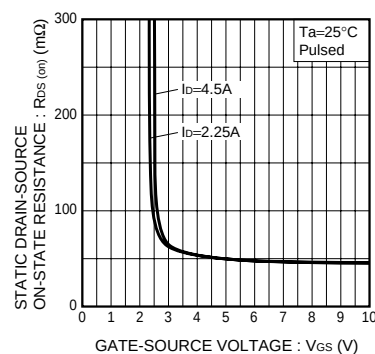


Fig.7 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

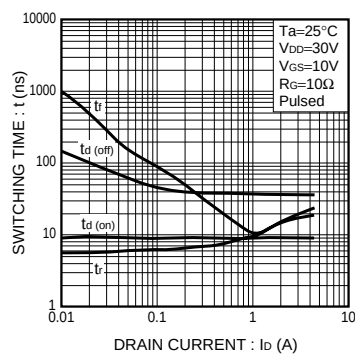


Fig.8 Switching Characteristics

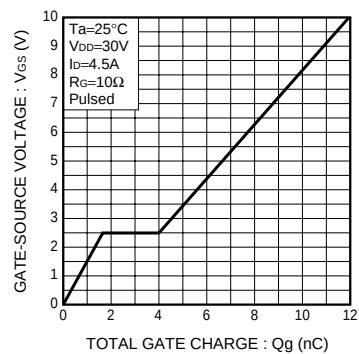


Fig.9 Dynamic Input Characteristics

Transistor

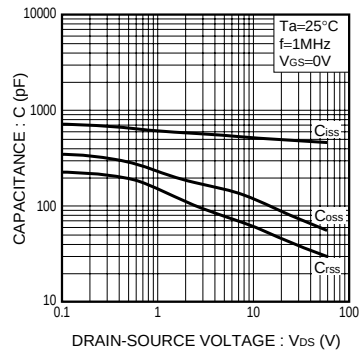


Fig.10 Typical Capacitance
vs. Drain-Source Voltage

●Measurement circuits

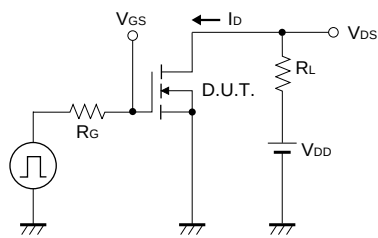


Fig.1-1 Switching Time Test Circuit

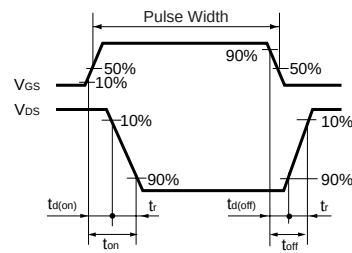


Fig.1-2 Switching Time Waveforms

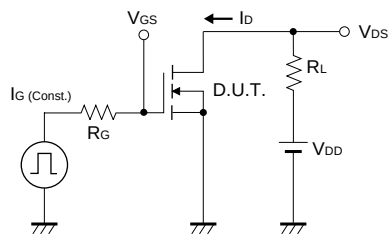


Fig.2-1 Gate Charge Test Circuit

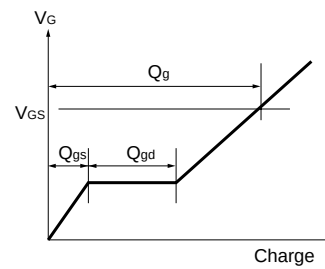


Fig.2-2 Gate Charge Waveform

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