

Description

The uses advanced SGT technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

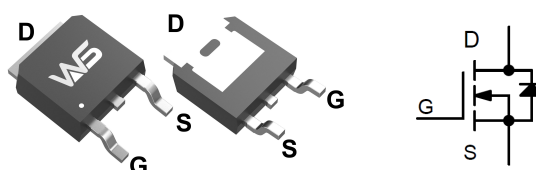
Application

- Consumer electronic power supply
- Motor control
- Synchronous-rectification
- Isolated DC

Product Summary

BVDSS	RDSON	ID
100V	13.8mΩ	40A

TO-252 Pin Configuration



Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain source voltage	100	V
VGS	Gate source voltage	±20	V
ID	Continuous drain current ¹⁾	TC=25 °C 40	A
ID, pulse	Pulsed drain current ²⁾	TC=25 °C 120	A
P _D	Power dissipation ³⁾	TC=25 °C 71	W
EAS	Single pulsed avalanche energy ⁵⁾	57	mJ
T _{stg} , T _j	Operation and storage temperature	-55 to 150	°C
R _{θJC}	Thermal resistance, junction-case	1.76	°C/W
R _{θJA}	Thermal resistance, junction-ambient ⁴⁾	62	°C/W

Electrical Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
BVDSS	Drain-source breakdown voltage	V _{GS} =0 V, I _D =250 μA	100	107	-	V
VGS(th)	Gate threshold voltage	V _{DS} =V _{GS} , I _D =250 μA	1.2	1.5	2.5	V
RDS(ON)	Drain-source on-state resistance	V _{GS} =10 V, I _D =10 A	-	13.8	20.0	mΩ
RDS(ON)	Drain-source on-state resistance	V _{GS} =4.5 V, I _D =7 A	-	17.4	26.0	mΩ
IGSS	Gate-source leakage current	V _{GS} =±20 V	-	-	±100	nA
IDSS	Drain-source leakage current	V _{DS} =100 V, V _{GS} =0 V	-	-	1	μA
Ciss	Input capacitance	V _{GS} =0 V, V _{DS} =50 V, f=100 kHz	-	1003.9	-	pF
Coss	Output capacitance		-	185.4	-	pF
Crss	Reverse transfer capacitance		-	9.8	-	pF
td(on)	Turn-on delay time	V _{GS} =10 V, V _{DS} =50 V, R _G =10 Ω, I _D =5 A	-	16.6	-	ns
t _r	Rise time		-	3.8	-	ns
td(off)	Turn-off delay time		-	75.5	-	ns
t _f	Fall time		-	46	-	ns
Q _g	Total gate charge	I _D =5 A, V _{DS} =50V, V _{GS} =10V	-	16.2	-	nc
Q _{gs}	Gate-source charge		-	2.8	-	nc
Q _{gd}	Gate-drain charge		-	4.1	-	nc
V _{plateau}	Gate plateau voltage		-	3	-	V
I _s	Diode forward current	V _{GS} <V _{th}	-	30	-	A
ISP	Pulsed source current		-	90	-	A
t _{rr}	Reverse recovery time	I _S =1A, di/dt=100 A/μs	49	-	-	ns
Q _{rr}	Reverse recovery charge		61.8	-	-	nc
I _{rrm}	Peak reverse recovery current		2.4	-	-	A

Note :

- 1、 Calculated continuous current based on maximum allowable junction temperature.
- 2、 Repetitive rating; pulse width limited by max. junction temperature.
- 3、 Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4、 The value of R_{θja} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.
- 5、 V_{DD}=50 V, R_G=25 Ω, L=0.3 mH, starting T_j=25 °C.

Typical Characteristics

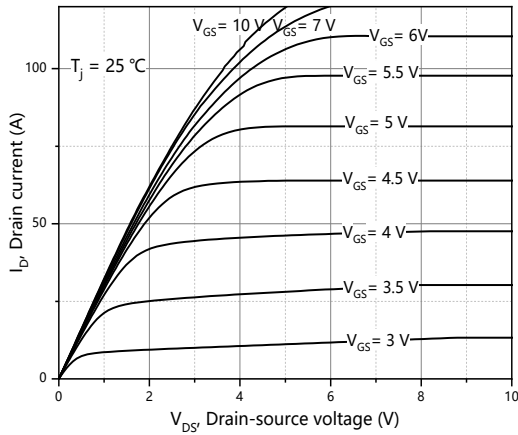


Figure 1, Typ. output characteristics

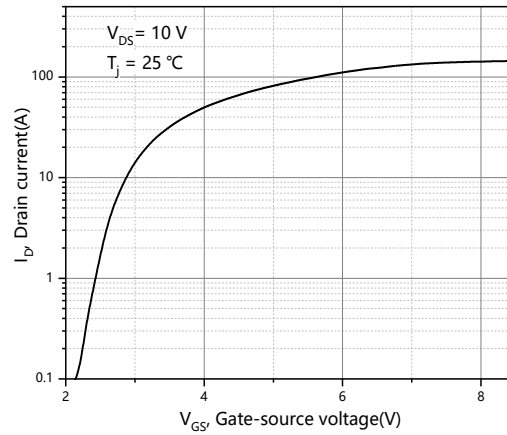


Figure 2, Typ. transfer characteristics

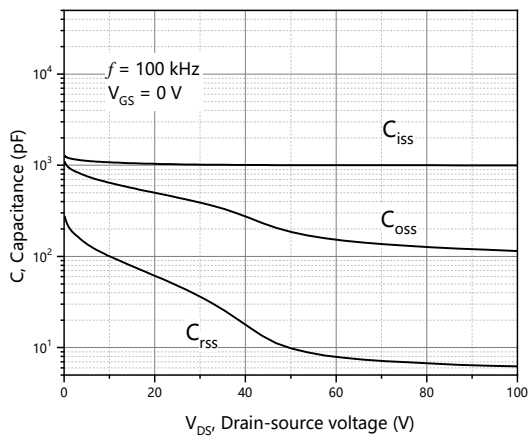


Figure 3, Typ. capacitances

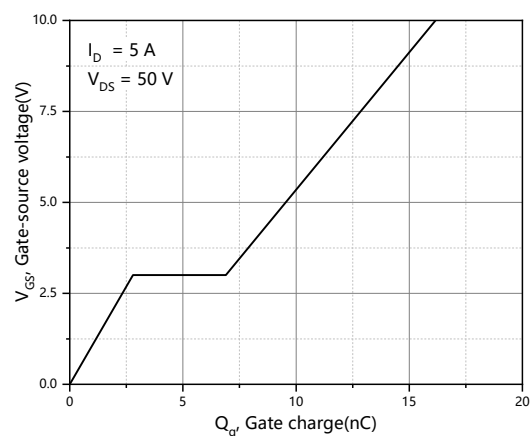


Figure 4, Typ. gate charge

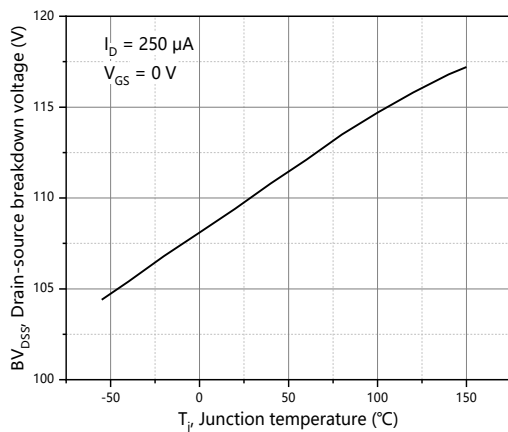


Figure 5, Drain-source breakdown voltage

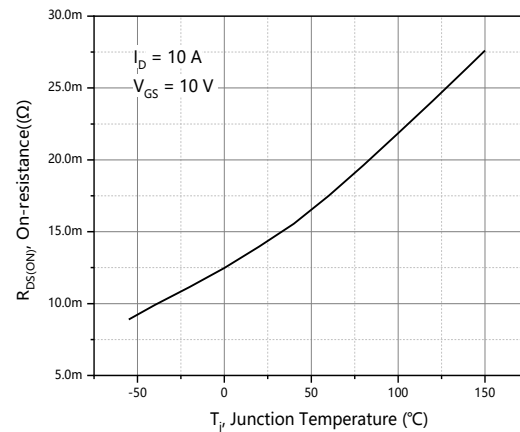


Figure 6, Drain-source on-state resistance

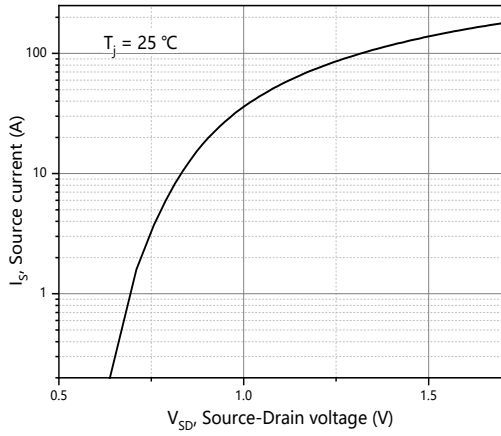


Figure 7, Forward characteristic of body diode

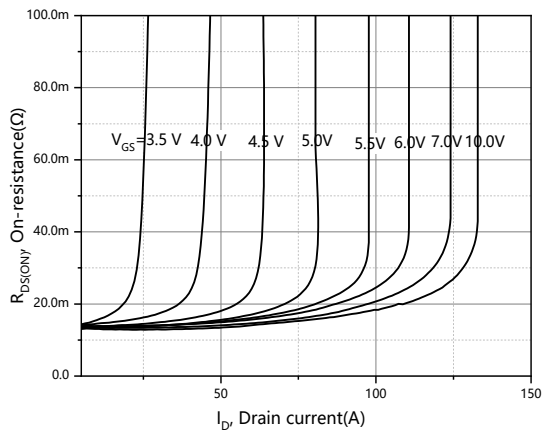


Figure 8, Drain-source on-state resistance

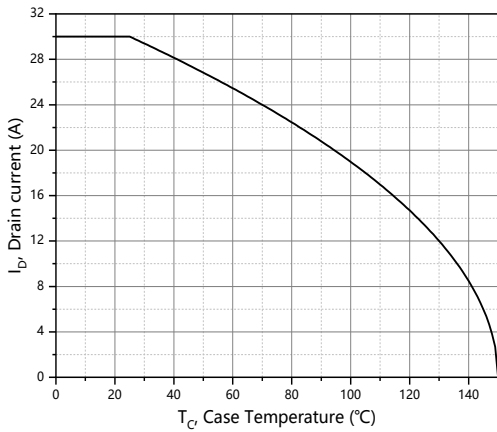


Figure 9, Drain current

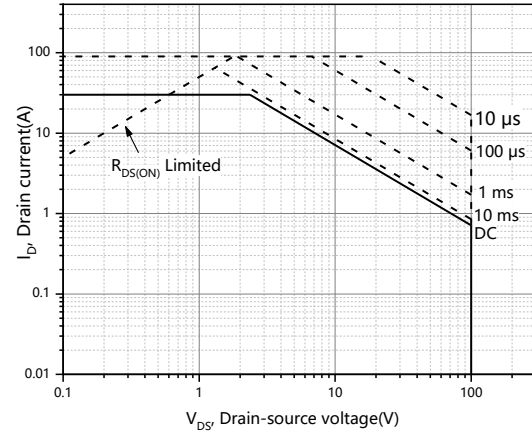


Figure 10, Safe operation area $T_C=25\text{ }^\circ\text{C}$

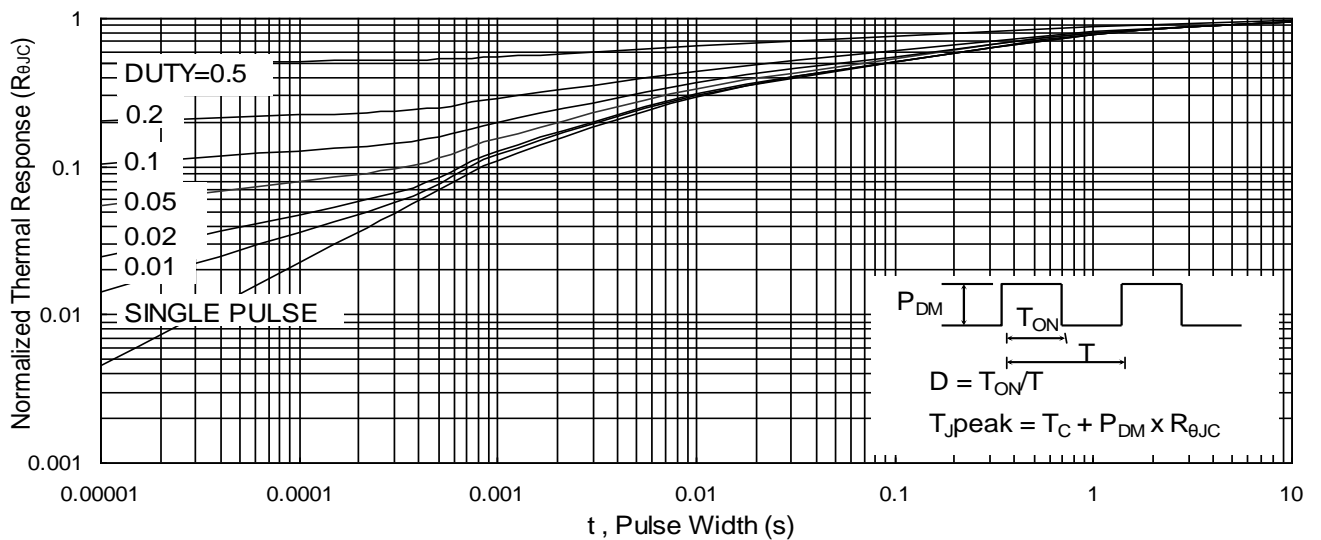


Fig11. Normalized Maximum Transient Thermal Impedance



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